

CHIP SHAPED ELECTRONIC DEVICE AND A METHOD OF PRODUCING
THE SAME

ABSTRACT OF THE DISCLOSURE

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A chip shaped electronic device comprising an element body including zinc oxide material layers and internal electrode layers, wherein when assuming a minimum distance from an outermost side of the internal
10 electrode layer in the stacking direction to a surface of the element body is l and measuring an ion intensity ratio of Li and Zn, (Li/Zn) , in a range from the surface of the element body to a depth of $(0.9 \times l)$ by a secondary ion mass spectrometry (SIMS), $0.001 \leq (\text{Li}/\text{Zn}) \leq$
15 500. According to the invention, it is possible to provide a chip shaped electronic device, such as a multilayer chip varistor, not requiring glass coating or other insulative protective layer, being tolerant of temperature changes, capable of maintaining high
20 resistance of an element surface even by reflow soldering, being highly reliable, and capable of being easily produced, and a method of producing the same.